



### Research Assistants at System on Chip (SoC) Lab SEECS NUST

We are currently seeking full-time and part-time Research Assistants to join our team with focus on Computer Architecture, System-on-Chip design, and Digital System Design. The candidate must have prior working and hands-on experience in the areas of Computer Architecture, Digital Systems Design and FPGAs. Applicants with strong background in the above-mentioned areas are encouraged to apply. General eligibility guidelines are as follows:

- Bachelors/Masters in Electrical/Electronic/Computer Engineering from HEC recognized University with minimum CGPA of 3.5. UG students currently in their final year and MS students in first year are also eligible to apply.
- Knowledge of digital design techniques and methodologies.
- Proficient in Verilog/System-Verilog HDL.
- Hands on working experience of FPGA.
- Knowledge of hardware implementations of data encryption algorithms would be a plus.
- Digital IC design back-end tools knowledge, working with deep sub-micron CMOS processes will be a plus.
- Knowledge of low power, multi-voltage design will be an added advantage.
- Programming or scripting language useful.
- Perform well under time pressure.
- Perform other duties at SoC lab as required.

#### How to Apply:

Please write a brief email about your related experience, and why you would be suitable for the job. Send the email along with your resume and transcript(s) at [soc@seecs.edu.pk](mailto:soc@seecs.edu.pk). Please apply before **23 January 2023**.

For more details about System on Chip (SoC) lab., please visit <https://soc.seecs.edu.pk>