

Emerging Technologies in Artificial Intelligence

Deep Learning Lab, National Center of Artificial Intelligence NUST along with international collaboration is bringing forth an opportunity to gain insights into the latest technologies and trends in Artificial Intelligence from the AI experts. Talks and discussions will be hosted between the students and the German Professors. The students are encouraged to participate and utilize this platform to gain exposure to recent research trends and ideas.

Venue: SEECS Seminar Hall

Timings: September 14, 2022 | 9:00 am to 1:00pm

Organizer: Dr. Faisal Shafait (*SEECS*), Dr. Muhammad Imran Malik (*SEECS*) and Dr. Adnan ul Hasan (*DLL, NCAI*)

International Collaborators:

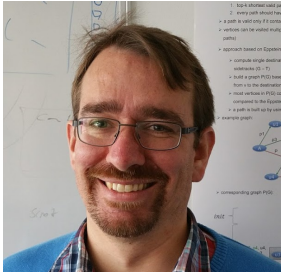


Hochschule **RheinMain**
University of Applied Sciences
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**TECHNISCHE UNIVERSITÄT
KAISERSLAUTERN**

About the Speakers



Prof. Dr. Adrian Ulges has been a professor at RheinMain University of Applied Sciences (HSRM) since 2013. His research interests are in machine learning, computer vision, and multimedia analysis. He graduated from TU Kaiserslautern. He received a diploma in computer science in 2005. In 2009, he completed his PhD in computer science. He remained an active researcher at the German Research Center for Artificial Intelligence (DFKI) in Kaiserslautern Germany from 2005 to 2011. He worked with Google as an intern in 2005, at Mountain View and as a visiting scientist in 2011 in Zurich. Adrian's work has been awarded a Google Research Award in 2010, and his publication record includes over 30 peer-reviewed papers.



Dr. Christian Weis received a Ph.D. degree in electrical engineering from the TU Kaiserslautern, Germany, in 2014. From 1996 to 1998, he was with Mitsubishi Semiconductor Europe, Germany, where he was engaged in the design and development of microcontrollers. From 1998 to 2009, he was with Siemens Semiconductor, Infineon Technologies AG and Qimonda AG, Munich, Germany, in DRAM design. During this time frame, he was involved in DRAM design for graphics and commodity DRAM products. In 2006, he was a Design Team Leader for the 1Gb DDR3 DRAM, the first DDR3 volume product at Infineon/Qimonda. Since 2009, he has been with the Microelectronic System Design Research Group, TU Kaiserslautern, Germany. He holds more than 20 patents related to DRAMs and DRAM design and published more than 70 papers. His current research interests include DRAM controller design, Near- & In-Memory processing, 3D-integrated DRAMs, heterogeneous memory architectures, and MPSoCs.



Prof. Dr. Ulrich Schwanecke has been a full professor for Computer Science in the Faculty of Design -Computer Science-Media at RheinMain University of Applied Sciences since 2003. He received his Master's degree in Mathematics and Computer Science from Johannes Gutenberg University Mainz in 1997. From 1997 to 2000 he worked as a research assistant at TU Darmstadt from where he received his Ph.D in 2000. Subsequently, he held a postdoctoral position for one year at the Max-Planck Institute for Computer Science in Saarbrücken. From 2001 to 2003 he worked as a researcher at Daimler AG in Ulm.