Job Title			Research Associate - Radar System Engineer		
No. of Positions			01		
	C	uration	3 Years		
School/Institute/Centre				SINES	
1	Purpose of Position				
	The radar system engineer is expected to study and come up with system requirements for railway applications. The individual shall liaison with other team members to generate system architecture, generate BOMs, carry out procurement and perform system integration and testing. The individual should have broad knowledge of radar sub-components including RF front-ends, FPGAs, signal processing etc.				
2		Functions/Du	ties (Key Respo	onsibilities)	
	Radar System Design Engineer should be able to: • Design system architecture suitable for railway applications including multitude of sensors like RADAR, LIDAR, Thermal and Optical Cameras • Identify modules, sub-systems suitable for chosen architecture • Perform system integration, lab and operational testing • Write project reports				
3		F	Requirements		
Essential Desired					
		Laseittiai		2 33.1. 34	
Qua	alifications	Bachelors in Electronics Engineering or relevant experience from HEC re institution	field with	Master's degree in EE or relevant field with experience in RF & Microwave	
	alifications sperience	Bachelors in Electronics Engineering or relevant experience from HEC re	field with ecognized	Master's degree in EE or relevant field with experience	
Ex Kn Skill:		Bachelors in Electronics Engineering or relevant experience from HEC re institution Min 1 year in simila Knowledge of radar, associated compleme Report writing skills Team management 8	field with ecognized ar role lidar, thermal a entary strength project managelectronics and cal/Electronics-	Master's degree in EE or relevant field with experience in RF & Microwave 2 years experience in similar role nd optical cameras and their s gement skills I processing platforms Industry standards	
Ex Kn Skill:	owledge/ s/Abilities/	Bachelors in Electronics Engineering or relevant experience from HEC re institution Min 1 year in simila Knowledge of radar, associated compleme Report writing skills Team management 8 Knowledge of power Knowledge of Electric Modeling of mechani	field with ecognized ar role lidar, thermal a entary strength project managelectronics and cal/Electronics-	Master's degree in EE or relevant field with experience in RF & Microwave 2 years experience in similar role nd optical cameras and their s gement skills I processing platforms Industry standards	

Job Title			Research Associate - RF / Antenna Design Engineer		
No. of Positions			01		
Duration			3 Years		
	School/In	stitute/Centre	SINES		
1	Purpose of Position				
	The RF / Antenna Design Engineer shall be responsible to design the RF front-end and antenna assembly of the radar system.				
2	Functions/Duties (Key Responsibilities)				
	RF Design Engineer should be able to: Design, Analyze and work with RF and antenna assemblies, modules & sub-systems Design PCBs Stuff and test the designed modules Handle RF equipment Knowledge of precautions for handling RF components and equipment				
3	Requirements				
		Essential	Desired		
Qu	alifications	Bachelors in Electronics/Electi Engineering or releva with experience fro	rical Master's degree in EE or int field relevant field with experience m HEC in RF & Microwave		
	alifications	Bachelors in Electronics/Electi Engineering or releva	rical Master's degree in EE or ant field relevant field with experience m HEC in RF & Microwave tion		
Ex Kri Skill		Bachelors in Electronics/Elect	Master's degree in EE or int field relevant field with experience in RF & Microwave tion A RF & 2 years experience in RF & Microwave A Antenna skills kills		
Ex Kri Skill	xperience nowledge/ ls/Abilities/	Bachelors in Electronics/Elect	Master's degree in EE or ant field relevant field with experience in RF & Microwave tion A RF & 2 years experience in RF & Microwave E / Antenna skills kills Kills E / EMC compliance standards equipment/instruments		

Job Title			Research Associate - Embedded System Engineer		
No. of Positions			01		
	Duration			3 Years	
	School/Institute/Centre			SINES	
1	Purpose of Position				
	The Embedded System Engineer shall be responsible to design and use embedded and power sub-systems for the overall radar system. The primary task of embedded design engineer is to map control and signal processing functions of radar on different hardware platforms. This includes knowledge of different processing platforms including FPGAs, embedded GPUs, DSPs.				
2	Functions/Duties (Key Responsibilities)			nsibilities)	
	 Embedded System Engineer should be able to: Extract control and processing platform requirements Hardware-software partitioning Work with FPGAs, microcontrollers, embedded GPUs, edge compute devices Map control, signal processing and AI modules on software and hardware Design embedded and power module circuits Integrate and test the designed modules with other sub-systems 				
3	Requirements				
		Essential		Desired	
Qualifications		Bachelors in Electronic Engineering or relevan experience from HEC institution	t field with	Master's degree in EE or relevant field with experience in Radar	
	Experience	Min 1 year in simil	ar role	2 years experience in Embedded Systems and/or Power Electronics	
Knowledge/ Skills/Abilities/ Competencies		 Digital system design using FPGAs (Verilog/VHDL) Microcontrollers software development and testing skills Power Electronics software and testing skills Circuit designing skills Intellectually curious, should be a quick learner and not hesitant to try new tools Report writing skills Able to use test equipment/instruments Able to communicate with other team members 			
4 How to Apply			w to Apply		
7		Please email your resume to projectposition@sines.nust.edu.pk only if you fulfil the criteria mentioned above with job title "Embedded System Engineer" in the subject field by 15 th Mach			

2022.

Job Title	Research Associate - Signal Processing / Sensor Fusion Design Engineer	
No. of Positions	01	
Duration	3 Years	
School/Institute/Centre	SINES	

1 Purpose of Position

The Design Engineer is required as a team member in an HEC funded project in the area of Signal Processing / Sensor Fusion. The primary responsibilities of the design engineer will include designing individual signal processing chains for variety of sensors including RADAR, LIDAR, optical and thermal cameras. Additionally, a sensor fusion architecture will also be designed and implemented for fusion of data from all these sensors.

2 Functions/Duties (Key Responsibilities)

Signal Processing / Sensor Fusion Design Engineer will be required to:

- Design of signal processing chains for individual sensors (RADAR, LIDAR, Optical, Thermal Cameras)
- Performance quantification of algorithms
- Perform data acquisition through multiple interfaces
- Analyze the sensor data
- Design target detection and classification algorithms using AI/ML
- Design sensor fusion algorithms and their performance quantification

3 Requirements

		Essential	Desired	
Q	Qualifications	Bachelors in Electronics/Electrical Engineering or relevant field with experience from HEC recognized institution	Master's degree in EE or relevant field with experience in Signal Processing and Sesnor Fusion	
Experience		Min 1 year in similar role	2 years experience in Sensor Signal Processing and Fusion	
	(nowledge/	 Hands on Digital Signal Processing Background knowledge of Comput Understanding of ML algorithms for Be able to acquire data from a vari Hands on experience of performing methods in MATLAB Simulink or of Be able to evaluate performance or 	er Vision and Image Processing or detection and classification ety of communication interfaces g signal processing simulations, filtering ther tools	
	kills/Abilities/ Competencies	 Understands the theory of operation and specification parameters of a variety of sensors Able to use test equipment/instruments Report writing and presentation skills Intellectually curious, should be a quick learner and not hesitant to try new tools Able to communicate with other team members 		
4		How to Apply		

Please email your resume to <u>projectposition@sines.nust.edu.pk</u> only if you fulfil the criteria mentioned above with job title "Signal Processing / Sensor Fusion Design Engineer" in the subject field by 15th Mach 2022.

	Job T	itle	Research Associate - Artificial Intelligence Engineer	
	No. of Po	sitions	01	
	Durat	ion	3 Year	s
	School/Institute/Centre		SINES	
1	Purpose of Position			
	The Design Engineer is required as a team member in an HEC funded project in the area of Applied AI. The main role of the engineer is to gather data related to the intended application and develop AI and sensor fusion algorithms followed by visualization dashboards.			
2	Functions/Duties (Key Responsibilities)			
	Write APIsDeploy anWork colla	 Design and Develop machine learning, computer vision and data fusion algorithms. Write APIs for AI algorithms. Deploy and optimize machine learning models/algorithms as scalable software. Work collaboratively in a cross-functional team environment. Design dashboards for algorithm prototyping. 		
3	Requirements			
		Essential		Desired
Qualifications Bachelors in Electronics/Electrical Engineering, Computer Engg, CS or relevant field with experience from HEC recognized institution			Engg, CS or rience from titution	
	•		th ROS, Tensorflow or PyTorch is a must.	
			erstanding of image processing techniques. wledge of Machine Learning concepts.	
			erstanding of sensor integration and data fusion	
4		Но	w to Apply	
	Please email your resume to projectposition@sines.nust.edu.pk only if you fulfil the criteria mentioned above with job title "Artificial Intelligence Engineer" in the subject field by 15 th Mach			

2022.