



### **Research Assistants Digital IC Design**

We are currently seeking full-time Research Assistants to join our team with focus on Computer Architecture, System-on-Chip design and Digital Integrated Circuit (IC) design. The candidate must have prior working and hands-on experience in the areas of Computer Architecture, Digital Systems Design, FPGAs, UVM Verification and Digital IC Design back-end tools (preferably Cadence). Applicants with strong background in the above mentioned areas are encouraged to apply at the Integrated Circuit Design Lab (ICDL) @ SEECs-NICHE, NUST.

- Bachelors/Masters in Electrical/Electronic/Computer Engineering from HEC recognized University with minimum CGPA of 3.3. UG students currently in their final year and MS students in first year are also eligible to apply.
- Proficient in Verilog/SystemVerilog HDL.
- Hands on working experience of FPGA.
- Knowledge of digital design techniques and methodologies.
- Knowledge of middle-ware and embedded OS.
- Hands on working experience of UVM based Verification will be an added advantage.
- Digital IC design back-end tools knowledge, working with deep sub-micron CMOS processes will be plus.
- Knowledge of low power, multi-voltage design will be an added advantage.
- Programming or scripting language useful.
- Perform well under time pressure.
- Perform other duties at ICDL lab as required.

#### **How to Apply:**

Please write to us about your related experience, and why you would be suitable for the job at [rehan.ahmed@seecs.edu.pk](mailto:rehan.ahmed@seecs.edu.pk). Apply before 10th November 2021.